

Fig. 1A

Test/load bus of CAM cell (n+1 bits) 120

100

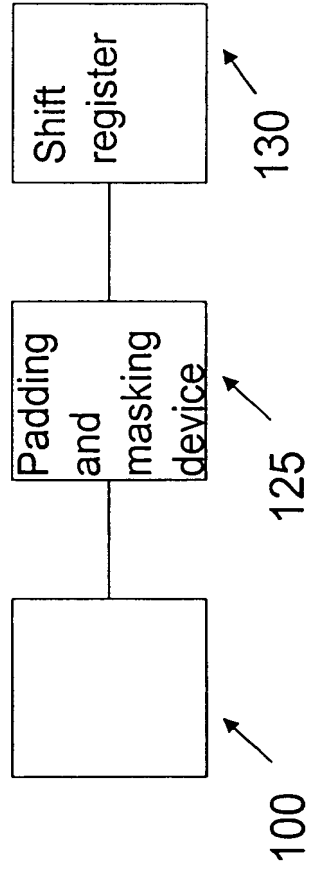


FIG. 1B

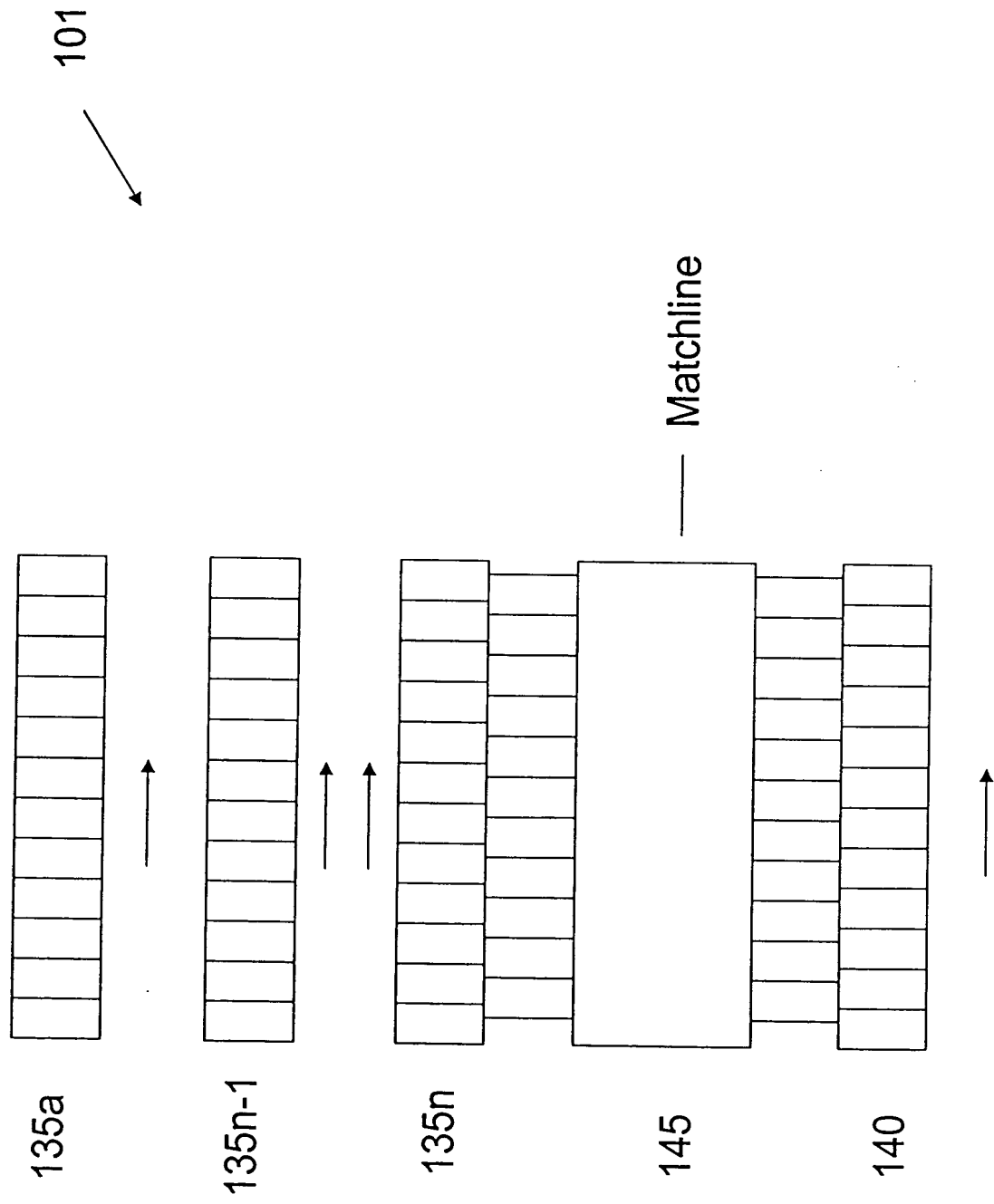


FIG. 1C

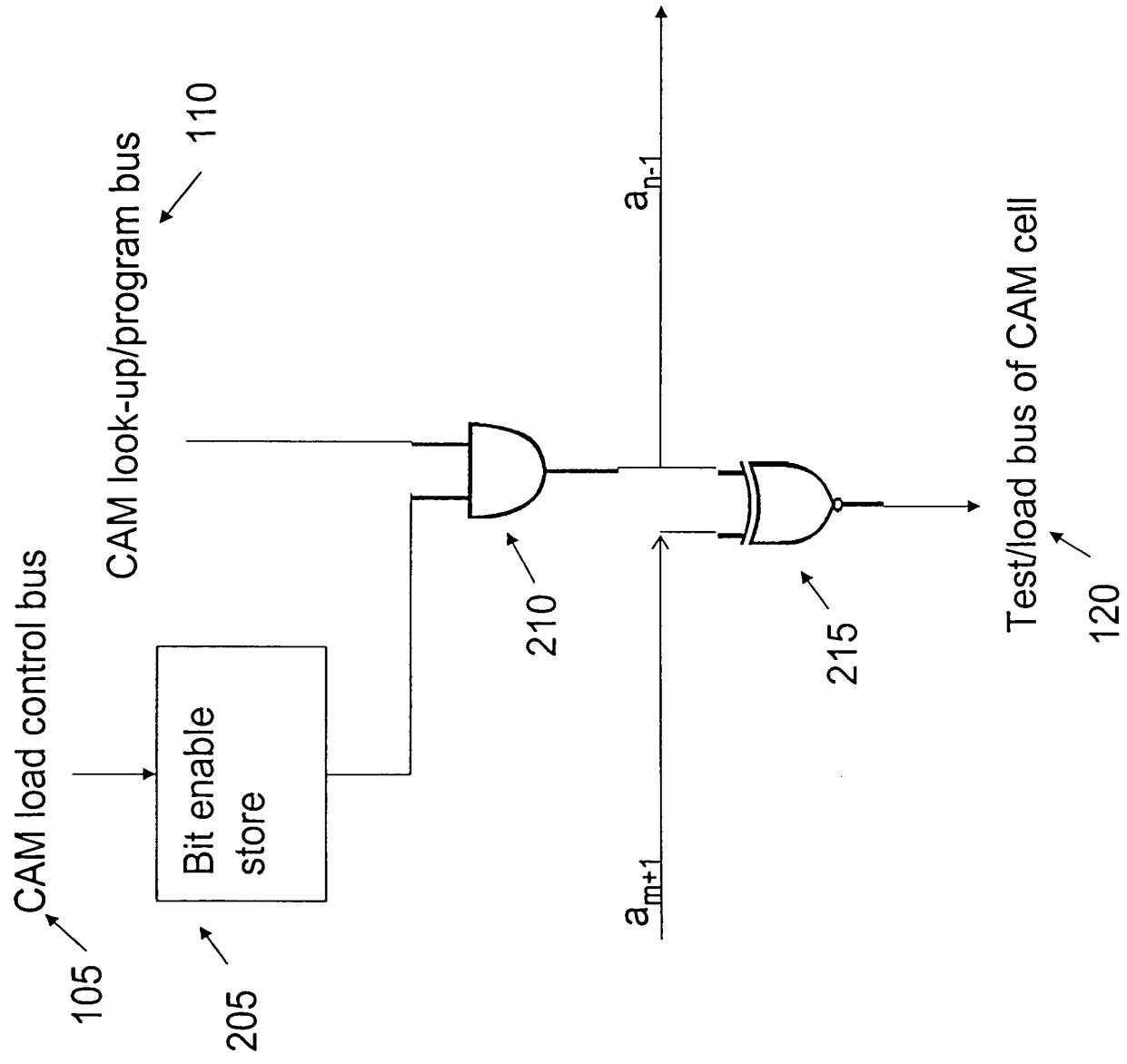
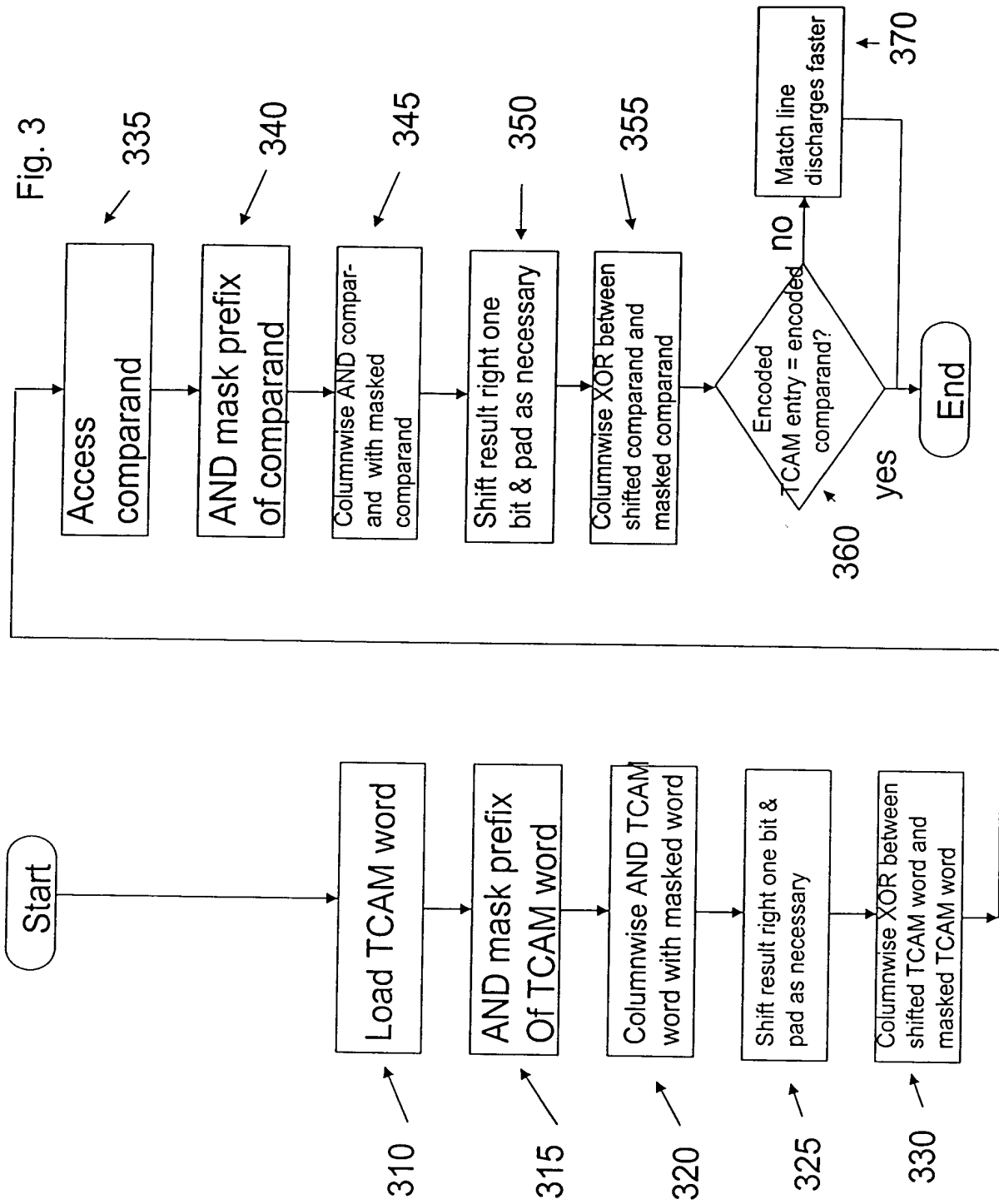


Fig. 2



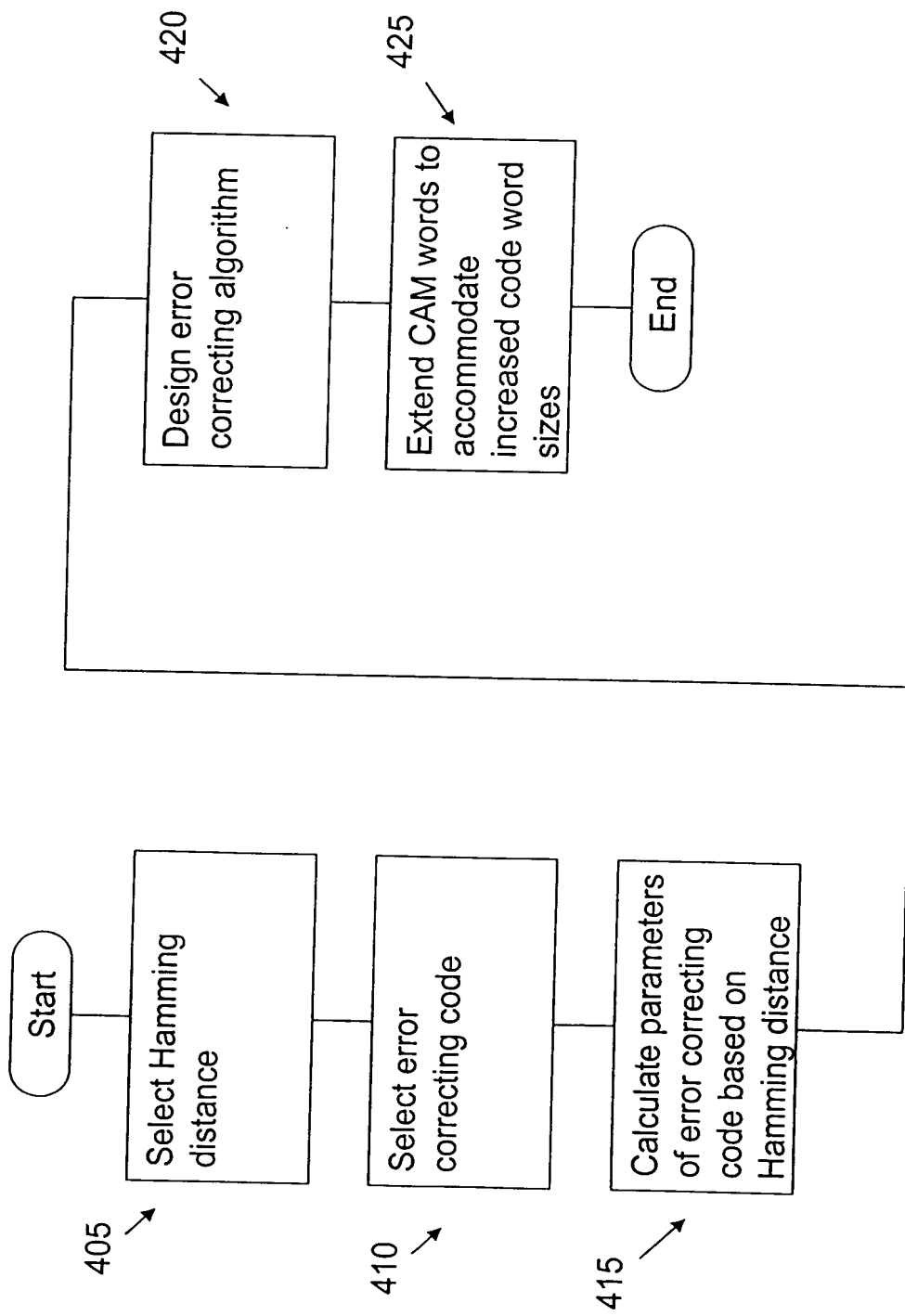


FIG. 4A

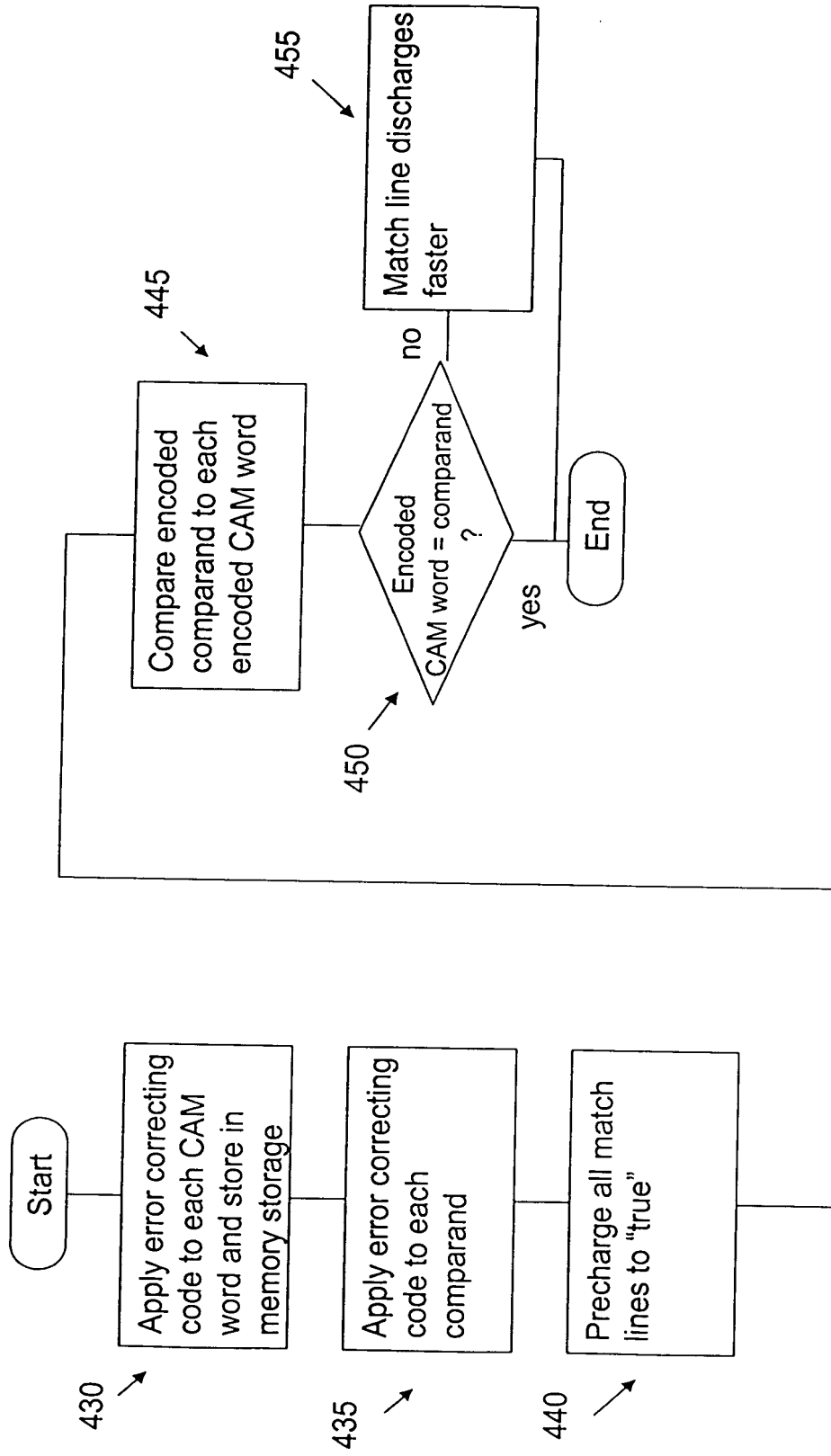


FIG. 4B

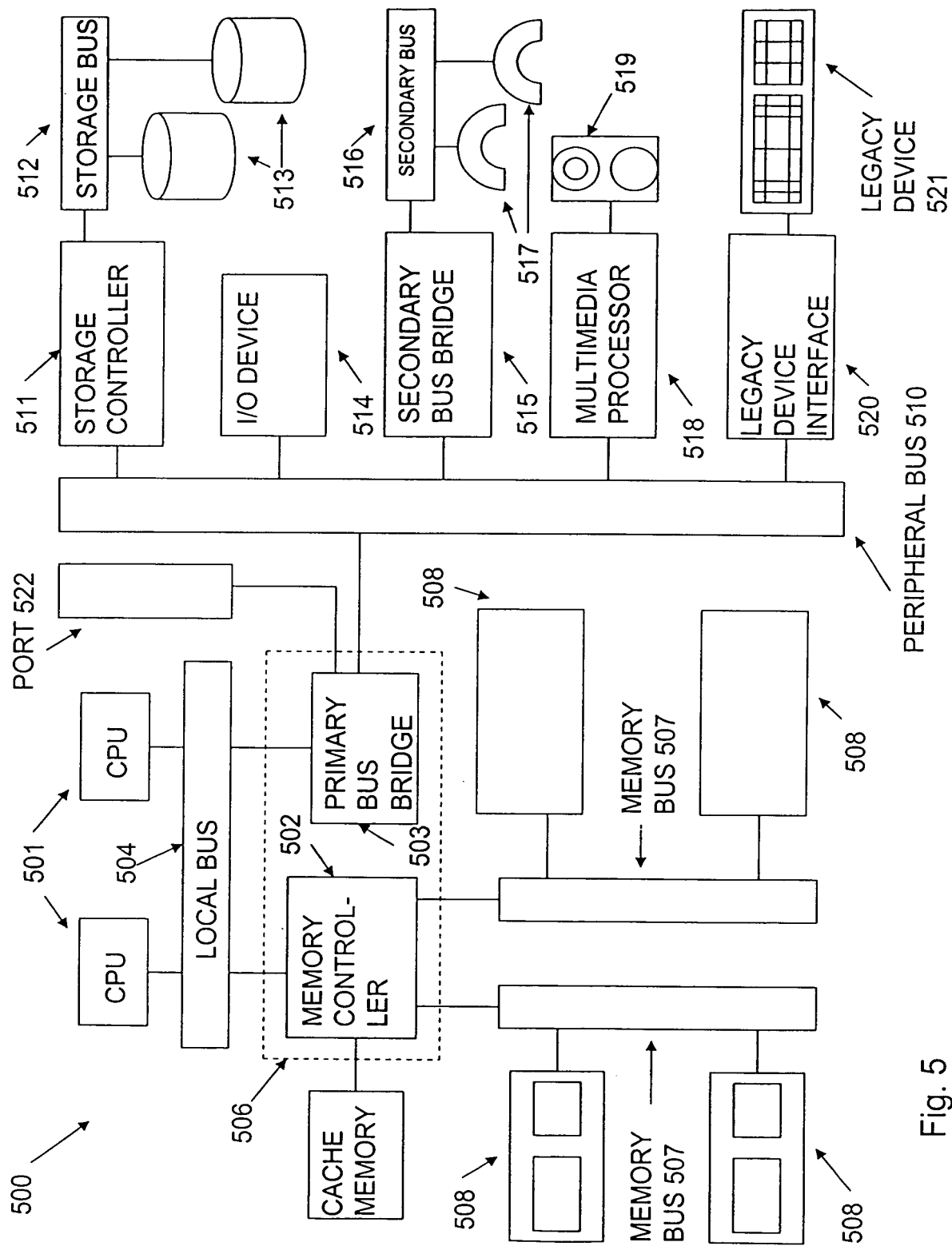


Fig. 5